

Form PTO-1449 (Modified)			ATTORNEY DOCKET NO.	ALTRP117/ A01404
INFORMATON DISCLOSURE STATEMENT BY APPLICANT			APPLICANT(S)	PRITCHARD et al.
(Use several sheets if necessary)			APPLICATION NO.	10/816,685
PAGE 1 OF 1			FILING DATE	4/1/2004
			ART UNIT	2825

US PATENT DOCUMENTS

EXAMINER INITIAL	CITE NO.	DOCUMENT NO.	DATE	PATENTEE	CLASS	SUB-CLASS	FILING DATE
	1	6052773	4/2000	DEHON et al.			
	2	6282706	8/2001	CHAUVEL et al.			
	3	6308234	10/2001	DAVIES et al.			
	4	6766460	7/2004	EVOY et al.			
	5	6993731	1/2006	WHITAKER et al.			
	6	7027972	4/2006	LEE			
	7	7120895	10/2006	YE et al.			
	8	7162591 A1	1/2007	MIRANDA et al.			
	9	2003/0233221 A1	12/2003	O'BRIEN et al.			
	10	2004/0015739	1/2004	HEINKEL et al.			
	11	2004/0130927 A1	7/2004	SCHULZ et al.			

NONPATENT DOCUMENTS

EXAMINER INITIAL	CITE NO.	AUTHOR, TITLE, PLACE (E.G. JOURNAL) OF PUBLICATION, DATE
	12	PRITCHARD et al. (11/16/2004), "Scheduling Logic on a Programmable Device Implemented Using a High-Level Language," US Patent Application No. 10/993,572 (ALTRP117X1).
	13	PRITCHARD et al. (9/28/2005), "Hardware Acceleration of High-Level Language Code Sequences on Programmable Devices," US Patent Application No. 11/238,451 (ALTRP158)
	14	US Office Action (1/3/2007) from US Patent Application No. 10/993,572 (ALTRP117X1).
	15	US Office Action (6/4/2007) from US Patent Application No. 11/238,451 (ALTRP158).
	16	US Office Action/Final Rejection (7/17/2007) from US Patent Application No. 10/993,572 (ALTRP117X1).

EXAMINER	DATE CONSIDERED

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.